

SO18W (RoHS compliant)

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FEATURES • MLT04 replacement • Four independent channels • Four-quadrant multiplication • Voltage output: W = 0.4 × X × Y • ±2.5 V analog input range • 3.5 MHz bandwidth • Low power dissipation • Analog computation • Squaring circuits • Modulation and demodulation • Voltage controlled amplifiers and filters • Analog computation • Modulation and demodulation • Low power dissipation • PACKAGES





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DESCRIPTION

iC-BM features four analog multipliers. Each four-quadrant multiplier consists of a Gilbert cell multiplier with a 0.4 scale factor, a linearisation circuit and a unity gain output amplifier.

For higher precision all internal bias currents are derived from an internal band-gap reference.

All pins are ESD protected.

PACKAGING INFORMATION

PIN CONFIGURATION SO18W



PIN FUNCTIONS

No. Name Function

1	W1	Channel 1: Analog multiplier output
2	GND1	Channel 1: Ground
3	X1	Channel 1: First input of multiplier
4	Y1	Channel 1: Second input of multiplier
5	VCC	Positive power supply +5 V
6	Y2	Channel 2: Second input of multiplier
7	X2	Channel 2: First input of multiplier
8	GND2	Channel 2: Ground
9	W2	Channel 2: Analog multiplier output
10	W3	Channel 3: Analog multiplier output
11	GND3	Channel 3: Ground
12	X3	Channel 3: First input of multiplier
13	Y3	Channel 3: Second input of multiplier
14	VEE	Negative power supply -5 V
15	Y4	Channel 4: Second input of multiplier
16	X4	Channel 4: First input of multiplier
17	GND4	Channel 4: Ground
18	W4	Channel 4: Analog multiplier output



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PACKAGE DIMENSIONS SO18W

All dimensions given in mm.

SIDE







RECOMMENDED PCB-FOOTPRINT



dra_so18w-1_pack_1, 5:1



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ABSOLUTE MAXIMUM RATINGS

Beyond these values damage may occur; device operation is not guaranteed.

Item	Symbol	Parameter	Conditions			Unit
No.				Min.	Max.	
G001	VCC	Positive Power Supply			7	V
G002	VEE	Negative Power Supply		-7		V
G003	V()	Voltage at Pins X_{14} , Y_{14} and W_{14}		-7	7	V
G004	Tj	Chip Temperature		-40	150	°C
G005	Ts	Storage Temperature		-40	150	°C

THERMAL DATA

Operating Conditions: VCC = 5 V ±0.25 V , VEE = -5 V ±0.25 V, Tj = -40...100 °C, $R_L = 2 k\Omega$, if not other specified

Item	Symbol	Parameter	Conditions			Unit	
No.	-			Min.	Тур.	Max.	
T01	Та	Operating Ambient Temperature Range		-40		85	°C
T02	Rthja	Thermal Resistance Chip/Ambient			68		K/W



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ELECTRICAL CHARACTERISTICS Operating Conditions: VCC = 5 V ±0.25 V, VEE = -5 V ±0.25 V, Tj = -40...100 °C, R_J = 2 kΩ, if not other specified ltem Symbol Conditions Unit Parameter No. Min. Typ. Max. General 101 V(VCC) Positive Supply Voltage Range 4.75 5 5.25 V 102 V(VEE) Negative Supply Voltage Range -5.25 -5 -4.75 V 20 103 I(VCC) Positive Supply Current W_{1 4} without load resistors 15 mΑ 104 I(VEE) Negative Supply Current W1...4 without load resistors -20 -15 mΑ Power Dissipation 200 mW $P_{DISS} = 5 V \times I_{CC} + 5 V \times I_{EE}$ 150 105 P_{DISS} **Multiplier Performance** Offset Voltage X1...4 $V(X_{1...4}) = 0 V, V(Y_{1...4}) = \pm 2.5 V$ 201 -50 50 mV V(X_{1...4})os 202 V(Y_{1...4})os Offset Voltage Y1...4 $V(Y_{1...4}) = 0 V, V(X_{1...4}) = \pm 2.5 V$ -50 50 mV 203 V(W_{1...4})os Output Offset Voltage W1 4 $V(X_{1...4}) = 0 V, V(Y_{1...4}) = 0 V$ -50 50 m٧ TCV()os $V(X_{1...4}) = 0 V, V(Y_{1...4}) = 0 V$ µV/°C Output Offset Drift W1...4 50 204 Fix Scale Factor 0.38 0.4 0.42 205 Κ $V(X_{1...4}) = \pm 2.5 V, V(Y_{1...4}) = \pm 2.5 V$ 1/V TE(X_{1...4}) Total Error X_{1...4} $-2.5 V \le X \le 2.5 V, Y = 2.5 V,$ -5 +2 5 % 206 measured as % of the ±2.5 V full scale Total Error Y1...4 207 TE(Y_{1...4}) -2.5V < Y < 2.5V, X = 2.5V,-5 ±2 5 % measured as % of the ±2.5 V full scale 208 TCE(X_{1...4}) Total Error Drift X_{1...4} $V(X_{1...4}) = -2.5 V, V(Y_{1...4}) = 2.5 V$ 0.005 %/°C TCE(Y_{1...4}) 209 Total Error Drift Y1...4 0.005 %/°C $V(Y_{1...4}) = -2.5 V, V(X_{1...4}) = 2.5 V$ 210 SE() Total Square Error X_{1...4}, Y_{1...4} $V(X_1) = V(Y_1), V(X_2) = V(Y_2), V(X_3) = V(Y_3)$ and 5 % $V(X_4) = V(Y_4)$ Linearity Error X1...4 $-2.5 V \le X \le 2.5 V, Y = 2.5 V$ % 211 $LE(X_{1...4})$ -1 ±0.2 1 212 LE(Y_{1...4}) Linearity Error Y_{1 4} -2.5 V < Y < 2.5 V, X = 2.5 V -1 ±0.2 % 1 **Dynamic Performance** Small Signal Bandwidth MHz 301 BW $V(W_{1...4}) = 0.1 V_{rms}$ 3.5 302 SR Slew Rate $V(W_{1...4}) = \pm 2.5 V$ 30 V/µs 303 $V(W_{1...4}) = \Delta 2.5 V$ and 1% error band ts Settling Time 1 μs 304 FT_{AC} AC Feedthrough $V(X_{1...4}) = 0 V, V(Y_{1...4}) = 1 V_{rms}$ and f = 1 kHz -65 dB CT_{AC} Crosstalk $V(X_{1...4}) = V(Y_{1...4}) = 1 V_{rms}, f = 100 \text{ kHz},$ 305 -90 dB applied to adjecent channel Outputs: W_{1...4} 401 Short Circuit Current ±30 lsc() mA THD(X_{1...4}) 402 Total Harmonic Distortion X_{1 4} f= 1 kHz, V(Y_{1...4}) = 2.5 V 0.1 % 403 0.02 % THD(Y_{1...4}) Total Harmonic Distortion Y_{1...4} $f = 1 \text{ kHz}, V(X_{1...4}) = 2.5 \text{ V}$ 404 PSSR() Power Supply Sensitivity Ratio $V(X_{1...4}) = V(Y_{1...4}) = 0 V, VCC = \Delta 5\%$ or 10 mV/V VEE = ∆5% BW = 10 Hz to 50 kHz 70 405 **EN**_A Audio Band Noise μV_{rms} 406 **EN**_W Wide Band Noise BW = 1.9 MHz 590 μV_{rms} Spot Noise Voltage Noise at f = 1 kHz 0.3 $\mu V / \sqrt{Hz}$ 407 en VCC = +5 V, VEE = -5 V 408 Vmax() Voltage Swing 3.0 3.3 V

409	ROUT()	Open Loop Output Resistance	VCC = +5 V, VEE = -5 V, T = +25 °C		60		Ω
Inputs	: X ₁₄ , Y ₁₄	l l					
501	VR()in	Analog Input Range	$V(GND_{14}) = 0V$	-2.5		2.5*	V
502	l()in	Input Current	$V(X_{14}) = V(Y_{14}) = 0 V$		2.3	10	μA
503	R()in	Input Resistance			1		MΩ
504	C()in	Input Capacitance			3		pF

* For input voltages > 3 V the output is undefined.



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ORDERING INFORMATION

Туре	Package	Order Designation
iC-BM	SO18W	iC-BM SO18W

Please send your purchase orders to our order handling team:

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