

## HIGH RELIABILITY HYBRID DC-DC CONVERTERS

#### **DESCRIPTION**

The DVHV series of high reliability DC-DC converters is operable over the full military (-55 °C to +125 °C) temperature range with no power derating. Unique to the DVHV series are robust and effective input and output filters which provide dramatically reduced input and output noise performance when compared to other manufacturers competing devices. Operating at a nominal fixed frequency of 475 kHz, these regulated, isolated units utilize a high speed magnetic feedback design and well controlled undervoltage lockout circuitry to eliminate slow start-up problems.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266 5,790,389 5,963,438 5,999,433 6,005,780 6,084,792 6,118,673

#### **FEATURES**

- High Reliability
- Very Low Output Noise
- Wide Input Voltage Range: 15 to 50 Volts per MIL-STD-704
- Up to 15 Watts Output Power
- Fault Tolerant Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- Industry Standard Pinout
- High Input Transient Voltage: 80 Volts for 1 sec per MIL-STD-704A
- Precision Seam Welded Hermetic Package
- Custom Versions Available
- Additional Environmental Screening Available
- Meets MIL-STD-461 Revisions C, D, E and F EMC Requirements When Used With VPT's EMI Filters
- Flanged and Non-flanged Versions Available.
- MIL-PRF-38534 Element Evaluated Components
- Space Applications should consider VPT's "S" Series of Radiation Tolerant Power Conversion Devices. Contact VPT for details.

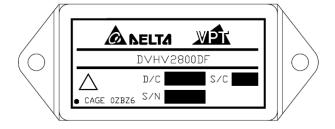


Figure 1 – DVHV2800D / DVHV2800DF DC-DC Converter (Exact marking may differ from that shown)

Sales Information: Phone: (425) 353-3010 Fax: (425) 353-4030 E-mail: vptsales@vpt-inc.com



**SPECIFICATIONS** ( $T_{CASE} = -55 \,^{\circ}\text{C}$  to  $+125 \,^{\circ}\text{C}$ ,  $V_{IN} = +28 \,^{\circ}\text{L}$  5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

**ABSOLUTE MAXIMUM RATINGS** 

Input Voltage (Continuous) 50  $V_{DC}$  Input Voltage (Transient, 1 second) 80 Volts Output Power<sup>1</sup> 15 Watts Power Dissipation (Full Load,  $T_{CASE} = +125\,^{\circ}C$ ) 6 Watts ESD Rating per MIL-PRF-38534 3A

Junction Temperature Rise to Case +15  $^{\circ}$ C Storage Temperature -65  $^{\circ}$ C to +150  $^{\circ}$ C

Lead Solder Temperature (10 seconds) 270 ℃

Weight (Maximum) (Un-Flanged / Flanged) (52 / 55) Grams

Parameter		Conditions	DVHV2805D			DVHV2812D			Units
		Conditions	Min	Тур	Max	Min	Тур	Max	Units
STATIC								-	
INPUT		Continuous	15	28	50	15	28	50	V
Voltage <sup>4</sup>		Transient, 1 sec	-	-	80	-	-	80	V
Current		Inhibited	-	-	6	-	-	6	mA
Current		No Load	-	-	90	-	-	90	mA
Ripple Current		Full Load <sup>5</sup> , 20Hz to 10MHz	ı	-	50	-	-	50	mA <sub>p-p</sub>
Inhibit Pin Input⁴			0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit V	oltage4		9.0	11.0	13.0	9.0	11.0	13.0	V
UVLO Turn On			12.0	-	14.8	12.0	-	14.8	V
UVLO Turn Off⁴			11.0	-	14.5	11.0	-	14.5	V
	+V <sub>OUT</sub>	T <sub>CASE</sub> = 25 ℃	4.95	5.0	5.05	11.88	12.0	12.12	V
OUTPUT	$+V_{OUT}$	T <sub>CASE</sub> = -55 °C to +125 °C	4.925	5.0	5.075	11.82	12.0	12.18	V
Voltage <sup>5</sup>	$-V_{OUT}$	T <sub>CASE</sub> = 25 ℃	4.925	5.0	5.075	11.82	12.0	12.18	V
	$-V_{OUT}$	T <sub>CASE</sub> = -55 °C to +125 °C	4.90	5.0	5.10	11.76	12.0	12.24	V
Power <sup>3,6</sup>	Total		0	-	15	0	-	15	W
Power	$\pm V_{\text{OUT}}$	Either Output	0	-	10.5	0	-	10.5	W
Current <sup>3,6</sup>	±V <sub>OUT</sub>	Either Output	0	-	2.1	0	-	0.875	Α
Ripple Voltage	±V <sub>OUT</sub>	Full Load <sup>5</sup> , 20Hz to 10MHz	-	-	30	-	-	40	$mV_{p-p}$
Line Demulation	+V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	20	-	-	20	mV
Line Regulation	$-V_{OUT}$	V <sub>IN</sub> = 16V to 40V	-	-	200	-	-	200	mV
Load Demilation	+V <sub>OUT</sub>	5% Load to Full Load <sup>5</sup>	-	-	50	-	-	50	mV
Load Regulation	$-V_{OUT}$	5% Load to Full Load <sup>5</sup>	-	-	200	-	-	200	mV
Cross Regulation	-V <sub>OUT</sub>	+Load 70%, -Load 30% +Load 30%, -Load 70%	-	-	500	-	-	500	mV
EFFICIENCY		Full Load <sup>5</sup>	74	-	-	76	-	-	%
LOAD FALL T DOWED DIOC	DATION	Overload <sup>4</sup>	-	-	11	-	-	11	W
LOAD FAULT POWER DISS	IPATION	Short Circuit	-	-	11	-	-	11	W
CAPACITIVE LOAD⁴		Either Output	-	-	500	-	-	500	μF
SWITCHING FREQUENCY			400	475	550	400	475	550	kHz
SYNC FREQUENCY RANGE	=	$V_H - V_L = 5V$ , DC = 20-80%	500	-	600	500	-	600	kHz
ISOLATION		500 V <sub>DC</sub> , T <sub>CASE</sub> = 25°C	100	-	-	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55 ℃	-	413	-	-	413	-	kHrs

See notes next page.



**SPECIFICATIONS** ( $T_{CASE} = -55 \,^{\circ}\text{C}$  to  $+125 \,^{\circ}\text{C}$ ,  $V_{IN} = +28 \,^{\circ}\text{L}$  5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS								
Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15℃					
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65℃ to +150℃					
Output Power <sup>1</sup>	15 Watts	Lead Solder Temperature (10 seconds)	270℃					
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	6 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(52 / 55) Grams					
ESD Rating per MIL-PRF-38534	3A							

Parameter		Conditions	DVHV2805D			DVHV2812D			Units
			Min	Тур	Max	Min	Тур	Max	Units
DYNAMIC									
Load Step Output Transient	±V <sub>OUT</sub>	Half Load to Full Load	-	-	200	-	-	300	$mV_{PK}$
Load Step Recovery <sup>2</sup>		Hall Load to Full Load	-	-	400	-	-	500	μSec
Line Step Output Transient <sup>4</sup>	±V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	300	600	-	500	900	$mV_PK$
Line Step Recovery <sup>2, 4</sup>		V <sub>IN</sub> = 10 V to 40 V	-	300	500	-	300	500	μSec
Turn On Delay	±V <sub>OUT</sub>	V = 0V to 28V	-	-	20	-	-	20	mSec
Turn On Overshoot		$V_{IN} = 0V \text{ to } 28V$	-	-	25	-	-	50	$mV_{PK}$

Notes: 1. Dependant on output voltage.

2. Time for output voltage to settle within 1% of its nominal value.

3. Derate linearly to 0 at 135 ℃.

4. Verified by qualification testing.

5. Half load at +V<sub>OUT</sub> and half load at -V<sub>OUT</sub>.
6. Up to 70% of the total power or current can be drawn from any one of the two outputs.



**SPECIFICATIONS** ( $T_{CASE} = -55 \,^{\circ}\text{C}$  to  $+125 \,^{\circ}\text{C}$ ,  $V_{IN} = +28 \,^{\circ}\text{L}$  5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS			
Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15℃
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65℃ to +150℃
Output Power <sup>1</sup>	15 Watts	Lead Solder Temperature (10 seconds)	270℃
Power Dissipation (Full Load, T <sub>CASE</sub> = +125 °C)	6 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(52 / 55) Grams
FSD Bating per MII -PBF-38534	3A		

Parameter		O a m diki a m a		VHV2815	D	Umita
		Conditions	Min	Тур	Max	Units
STATIC					-	3
INPUT		Continuous	15	28	50	V
Voltage <sup>4</sup>		Transient, 1 sec	-	-	80	V
Current		Inhibited	-	-	6	mA
Ourient		No Load	-	-	90	mA
Ripple Current		Full Load <sup>5</sup> , 20Hz to 10MHz	-	-	50	mA <sub>p-p</sub>
Inhibit Pin Input⁴			0	-	1.5	V
Inhibit Pin Open Circuit V	oltage4		9.0	11.0	13.0	V
UVLO Turn On			12.0	-	14.8	V
UVLO Turn Off <sup>4</sup>			11.0	-	14.5	V
	+V <sub>OUT</sub>	T <sub>CASE</sub> = 25 ℃	14.85	15.0	15.15	V
OUTPUT	$+V_{OUT}$	T <sub>CASE</sub> = -55 °C to +125 °C	14.775	15.0	15.225	V
Voltage <sup>5</sup>	$-V_{OUT}$	T <sub>CASE</sub> = 25 ℃	14.775	15.0	15.225	V
	$-V_{OUT}$	T <sub>CASE</sub> = -55 °C to +125 °C	14.70	15.0	15.30	V
Power <sup>3,6</sup>	Total		-	-	15	W
Power	$\pm V_{\text{OUT}}$	Either Output	-	-	10.5	W
Current <sup>3,6</sup>	±V <sub>OUT</sub>	Either Output	-	-	0.7	Α
Ripple Voltage	±V <sub>OUT</sub>	Full Load <sup>5</sup> , 20Hz to 10MHz	-	-	40	mV <sub>p-p</sub>
Line Degulation	+V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	20	mV
Line Regulation	$-V_{OUT}$	V <sub>IN</sub> = 16V to 40V	-	-	200	mV
Load Degulation	+V <sub>OUT</sub>	5% Load to Full Load <sup>5</sup>	-	-	50	mV
Load Regulation	$-V_{OUT}$	5% Load to Full Load <sup>5</sup>	-	-	200	mV
Cross Regulation -V <sub>OUT</sub>		+Load 70%, -Load 30% +Load 30%, -Load 70%	-	-	500	mV
EFFICIENCY		Full Load⁵	77	-	-	%
LOAD FALL T DOWED DIOG	IDATION	Overload <sup>4</sup>	-	-	11	W
LOAD FAULT POWER DISSIPATION		Short Circuit	-	-	11	W
CAPACITIVE LOAD <sup>4</sup>		Either Output	-	-	500	μF
SWITCHING FREQUENCY			400	475	550	kHz
SYNC FREQUENCY RANGE		$V_H - V_L = 5V$ , DC = 20-80%	500	-	600	kHz
ISOLATION		500 V <sub>DC</sub> , T <sub>CASE</sub> = 25°C	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55 ℃	-	413	-	kHrs

See notes next page.



**SPECIFICATIONS** ( $T_{CASE} = -55 \,^{\circ}\text{C}$  to  $+125 \,^{\circ}\text{C}$ ,  $V_{IN} = +28 \,^{\circ}\text{L}$  5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS								
Input Voltage (Continuous)	50 V <sub>DC</sub>	Junction Temperature Rise to Case	+15℃					
Input Voltage (Transient, 1 second)	80 Volts	Storage Temperature	-65℃ to +150℃					
Output Power <sup>1</sup>	15 Watts	Lead Solder Temperature (10 seconds)	270℃					
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	6 Watts	Weight (Maximum) (Un-Flanged / Flanged)	(52 / 55) Grams					
ESD Rating per MIL-PRF-38534	3A							

Parameter		Conditions	ı	Units		
		Conditions	Min	Тур	Max	Ullits
DYNAMIC						
Load Step Output Transient	±V <sub>OUT</sub>	Half Load to Full Load	-	-	350	$mV_{PK}$
Load Step Recovery <sup>2</sup>		Hall Load to Full Load	-	-	500	μSec
Line Step Output Transient <sup>4</sup>	±V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	500	900	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>		$V_{\rm IN} = 10 V (0.40 V)$	-	300	500	μSec
Turn On Delay	±V <sub>OUT</sub>	$V_{IN} = 0V$ to 28V	-	-	20	mSec
Turn On Overshoot		VIN = UV IU ZUV	-	-	50	$mV_{PK}$

- Notes: 1. Dependant on output voltage.
  - 2. Time for output voltage to settle within 1% of its nominal value.
  - 3. Derate linearly to 0 at 135 ℃.
  - 4. Verified by qualification testing.

  - 5. Half load at +V<sub>OUT</sub> and half load at -V<sub>OUT</sub>.
    6. Up to 70% of the total power or current can be drawn from any one of the two outputs.



#### **BLOCK DIAGRAM**

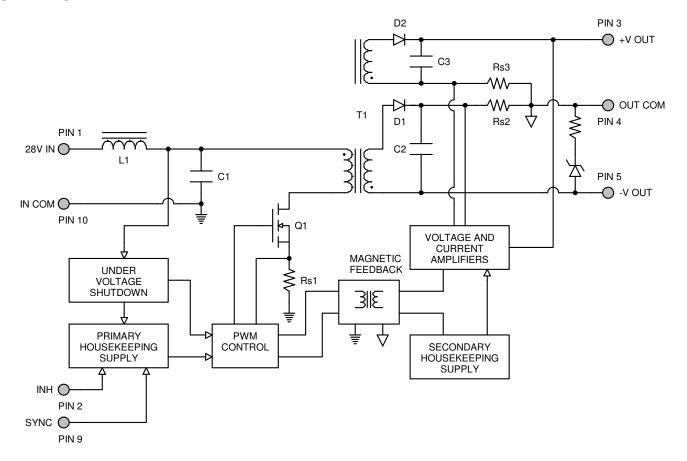


Figure 2

#### **CONNECTION DIAGRAM**

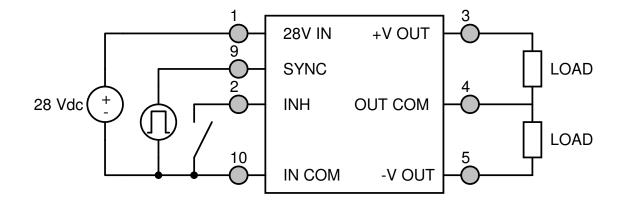
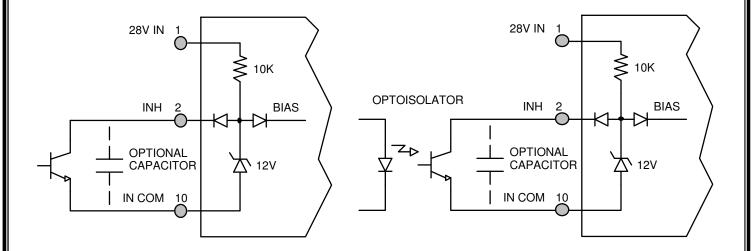


Figure 3



#### **INHIBIT DRIVE CONNECTION DIAGRAMS**



**Figure 4** – Internal Inhibit Circuit and Recommended Drive (Shown with optional capacitor for turn-on delay)

Figure 5 – Isolated Inhibit Drive (Shown with optional capacitor for turn-on delay)

#### **EMI FILTER HOOKUP DIAGRAM**

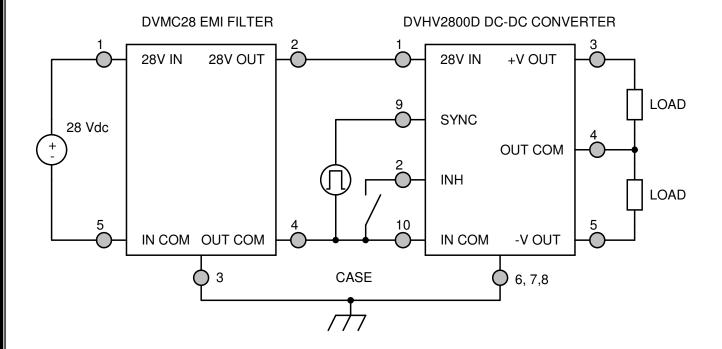
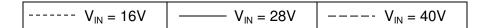


Figure 6 - Converter with EMI Filter



### **EFFICIENCY PERFORMANCE CURVES** (T<sub>CASE</sub> = 25 °C, Full Load, Unless Otherwise Specified)



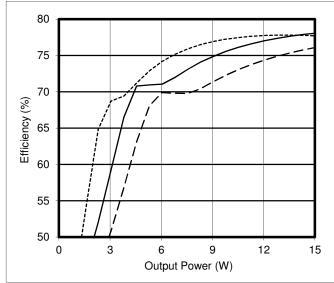


Figure 7 – DVHV2805D Efficiency (%) vs. Output Power (W)

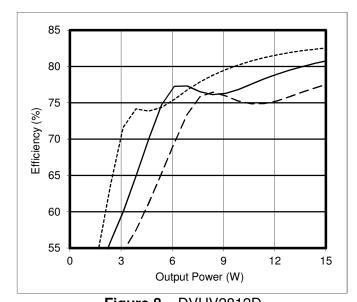


Figure 8 – DVHV2812D Efficiency (%) vs. Output Power (W)

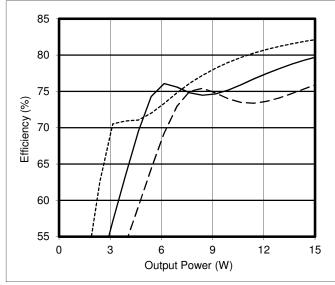
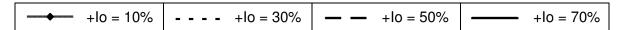


Figure 9 – DVHV2815D Efficiency (%) vs. Output Power (W)



CROSS REGULATION CURVES (T<sub>CASE</sub> = 25 °C, Full Load, Unless Otherwise Specified)



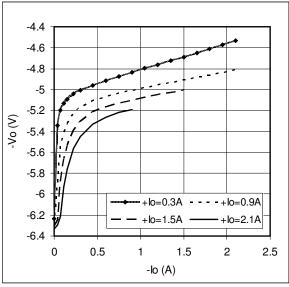


Figure 10 – DVHV2805D -Vout (V) vs. -lout (A)

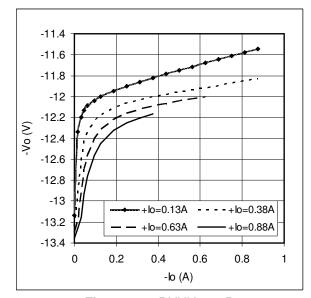


Figure 11 – DVHV2812D -Vout (V) vs. -lout (A)

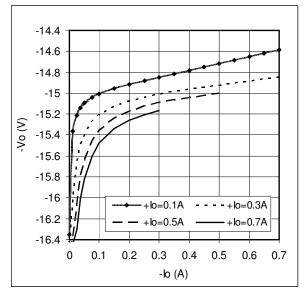


Figure 12 - DVHV2815D -Vout (V) vs. -lout (A)



#### **EMI PERFORMANCE CURVES**

(T<sub>CASE</sub> = 25 °C, V<sub>IN</sub> = +28V ± 5%, Full Load, Unless Otherwise Specified)

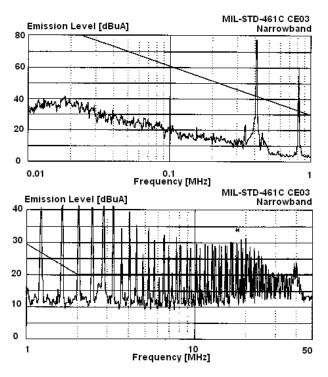


Figure 13 - DVHV2800D without EMI Filter

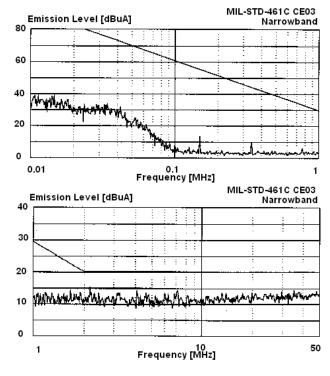
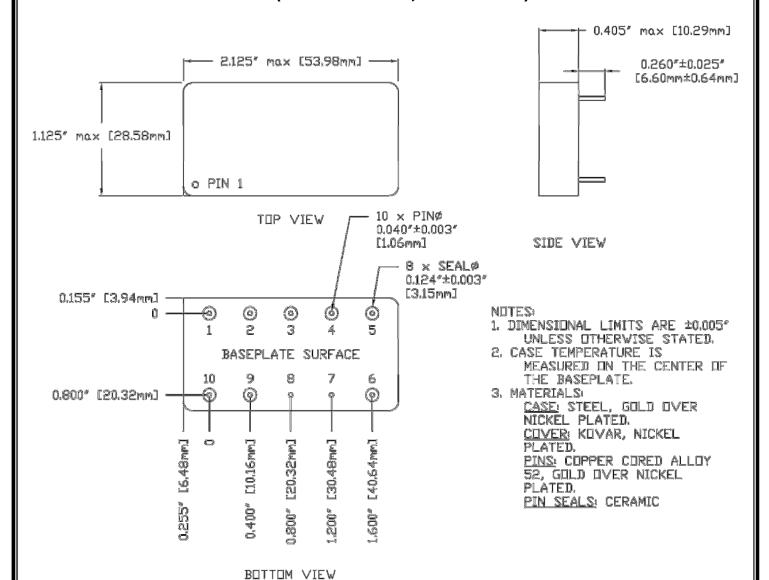


Figure 14 - DVHV2800D with EMI Filter



## PACKAGE SPECIFICATIONS (NON-FLANGED, SEAM SEAL)

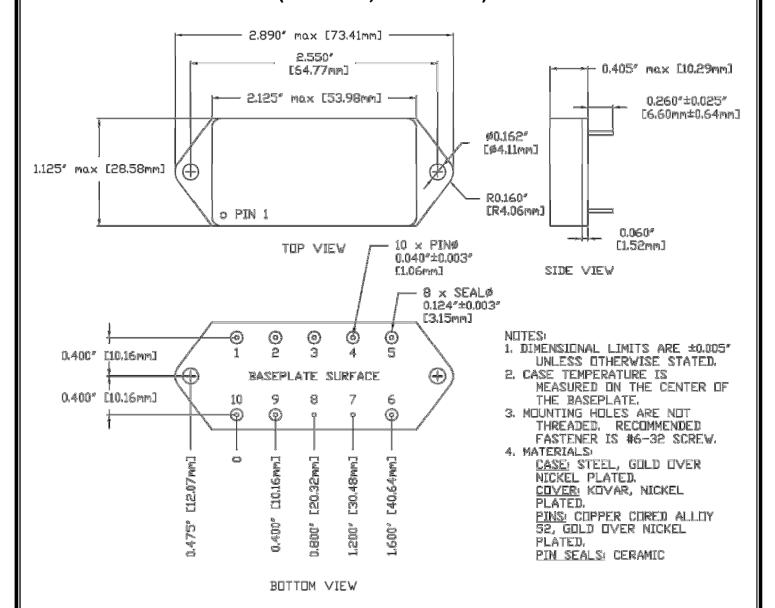


Pin	Function	Pin	Function
1	28V IN	6	CASE
2	INHIBIT	7	CASE
3	+V OUT	8	CASE
4	OUT COM	9	SYNC
5	-V OUT	10	IN COM

Figure 15 – Non-Flanged, Seam Seal Package and Pinout



## **PACKAGE SPECIFICATIONS (FLANGED, SEAM SEAL)**



Pin	Function	Pin	Function	
1	28V IN	6	CASE	
2	INHIBIT	7	CASE	
3	+V OUT	8	CASE	
4	OUT COM	9	SYNC	
5	-V OUT	10	IN COM	

Figure 16 - Flanged, Seam Seal Package and Pinout



### **PACKAGE PIN DESCRIPTION**

Pin	Function	Description
1	28V IN	Positive Input Voltage Connection
2	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to input common causes converter shutdown.  Logic High = Enabled Output. Unconnected or open collector TTL.
3	+V OUT	Positive Output Voltage Connection
4	OUT COM	Output Common Connection
5	-V OUT	Negative Output Voltage Connection
6	CASE	Case Connection
7	CASE	Case Connection
8	CASE	Case Connection
9	SYNC	Synchronization Signal
10	IN COM	Input Common Connection





### **ENVIRONMENTAL SCREENING** (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

Test	MIL-STD-883 Test Method, Condition	No Suffix (Standard) Non-QML ⑤	/ES (Extended) Non-QML ⑤	/H (Class H)	/K (Class K)
Non-Destructive Bond Pull	TM2023	• (4)	• (4)	• (4)	•
Internal Visual	TM2010, TM2017, TM2032 (MIL-STD-750, TM2072, TM2073)	•	•	•	•
Temperature Cycling	TM1010, Condition C -65 °C to 150 °C, Ambient TM1010, Condition B -55 °C to 125 °C, Ambient		•	•	•
Constant Acceleration	TM2001, 3000g, Y1 Direction TM2001, 500g, Y1 Direction		•	•	•
PIND ⑦	TM2020, Condition A				•
Pre Burn-In Electrical	25℃				•
Burn-In	TM1015, 320 hrs, 125 ℃, Case Typ TM1015, 160 hrs, 125 ℃, Case Typ 96 hrs, 125 ℃, Case Typ 24 hrs, 125 ℃, Case Typ	•	•	•	•
Final Electrical	MIL-PRF-38534, Group A Subgroups 1-6 -55℃, 25℃, 125℃ ③			•	•
	MIL-PRF-38534, Group A Subgroups 1 and 4 25℃	•	•		
Hermeticity (Seal)	TM1014, Fine Leak, Condition A2 TM1014, Gross Leak, Condition C Gross Leak, Dip (1 x 10 <sup>-3</sup> )	•	•	•	•
Radiography ®	TM2012				•
External Visual	TM2009	•	•	•	•

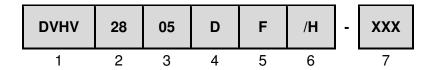
Notes:

- Contact Sales for more information concerning additional environmental screening and testing options desired.
- VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.
- 100% R&R testing with all test data included in product shipment.

  Not required per MIL-PRF-38534. Test is performed for additional product quality assurance.
- Non-QML products may not meet all requirements of MIL-PRF-38534.
- Note intentionally not used.
- PIND test Certificate of Compliance included in product shipment.
- Radiographic test Certificate of Compliance and film(s) or data CD included in product shipment.



#### **ORDERING INFORMATION**



(1) (2) (3)

Product Series	Nominal Input Voltage		Output Voltage		Number of Outputs	
DVHV	28	28 Volts	05 12 15	± 5 Volts ± 12 Volts ± 15 Volts	D	Dual

(5) (6) (7)

		(5)		( · )
Package Option		Screening Code <sup>1</sup>		Additional Screening Code
None F	Non-Flanged Flanged	None /ES /H /K	Standard Extended Class H Class K	Contact Sales

Notes: 1. VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.



### **SMD (STANDARD MICROCIRCUIT DRAWING) NUMBERS**

Standard Microcircuit	DVHV2800D Series	
Drawing (SMD)	Similar Part Number	
5962-1122701HXC 5962-1122701HXA 5962-1122701HYC 5962-1122701HYA 5962-1122701KXC 5962-1122701KXA 5962-1122701KYC 5962-1122701KYA	DVHV2805D/H DVHV2805D/H-E DVHV2805DF/H DVHV2805DF/H-E DVHV2805D/K DVHV2805D/K-E DVHV2805DF/K DVHV2805DF/K	
5962-1122702HXC 5962-1122702HXA 5962-1122702HYC 5962-1122702HYA 5962-1122702KXC 5962-1122702KXA 5962-1122702KYC 5962-1122702KYA	DVHV2812D/H DVHV2812D/H-E DVHV2812DF/H DVHV2812DF/H-E DVHV2812D/K DVHV2812D/K-E DVHV2812DF/K DVHV2812DF/K	
5962-1122703HXC	DVHV2815D/H	
5962-1122703HXA	DVHV2815D/H-E	
5962-1122703HYC	DVHV2815DF/H	
5962-1122703HYA	DVHV2815DF/H-E	
5962-1122703KXC	DVHV2815D/K	
5962-1122703KXA	DVHV2815D/K-E	
5962-1122703KYC	DVHV2815DF/K	
5962-1122703KYA	DVHV2815DF/K	

Do not use the DVHV2800D Series similar part number for SMD product acquisition. It is listed for reference only. For exact specifications for the SMD product, refer to the SMD drawing. SMD's can be downloaded from the DLA Land and Maritime (Previously known as DSCC) website at <a href="https://landandmaritimeapps.dla.mil/programs/defaultapps.asp">https://landandmaritimeapps.dla.mil/programs/defaultapps.asp</a>. The SMD number listed above is for standard gold plated lead

finish and no RHA (Radiation Hardness Assurance) level. Please reference the SMD for other screening levels, lead finishes, and radiation levels. All SMD products are marked with a "Q" on the cover as specified by the QML certification mark requirement of MIL-PRF-38534.



#### **CONTACT INFORMATION**

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

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